

# LEAD Institute of Academics

## Weightage of Marks in GATE (Subject-wise)

### GATE – EE (Electrical Engineering)

Subject	1 Mark Questions	2 Mark Questions	Total Marks
Engineering Mathematics	0 – 4	5 – 8	10 - 17
Electric Circuits	1 – 4	6 – 7	14 - 16
Electromagnetic Fields	0 – 2	0 – 4	2 - 9
Signals & Systems	0 – 3	0 – 7	0 - 16
Electrical Machines	3 – 6	8 – 12	19 – 27
Power Systems	3 – 6	8 – 11	19 - 27
Control Systems	1 – 3	3 – 7	7 – 17
Electrical & Electronic Measurements	1 – 3	1 – 8	3 – 19
Analog & Digital Electronics	1 – 5	4 – 10	12 - 25
Power Electronics & Drives	2 – 4	5 – 8	14 – 18
General Aptitude	5	5	15

Note: Analysis is made on the basis of previous **NINE** GATE Papers (2003 – 2011)

#### Examination Type:

GATE Examination consists of a single paper of **3 Hours** duration, which contains **65 Questions** carrying a maximum of **100 Marks**. The Question paper will consist of only multiple choice objective questions

Paper Code	Patterns of Question papers	Negative Marks for wrong Answer
CS, EC, EE, IN, CE, ME	Q.1 to Q.25: <i>Will carry one mark each</i> (sub-total 25 marks).	$\frac{1}{3}$ mark will be deducted for each wrong answer.
	Q.26 to Q.55: <i>Will carry two marks each</i> (sub-total 60 marks)	$\frac{2}{3}$ mark will be deducted for each wrong answer.
	Q.48 through Q.51 (2 pairs) will be <u>common data questions</u> . <i>Each question will carry two marks</i>	$\frac{2}{3}$ mark will be deducted for each wrong answer.
	Question pairs (Q.52, Q.53) and (Q.54, Q.55) will be <u>linked answer questions</u> . The answer to the second question of the last two pairs will depend on the answer to the first question of the pair. If the first question in the linked pair is wrongly answered or is un-attempted, then the answer to the second question in the pair will not be evaluated. <i>Each question will carry two marks</i>	There will be negative marks only for wrong answer to the first question of the linked answer question pair i.e. for Q.52 and Q.54, $\frac{2}{3}$ mark will be deducted for each wrong answer. There is no negative marking for Q.53 and Q.55.
	Q.56 to Q.60 : From General Aptitude (GA) <i>will carry one mark each</i> (sub-total 5 marks).	$\frac{1}{3}$ mark will be deducted for each wrong answer.
	Q.61 to Q.65 : From GA <i>will carry two marks each</i> (sub-total 10 marks)	$\frac{2}{3}$ mark will be deducted for each wrong answer.
All the papers bearing the codes CS, EC, EE, IN, CE, ME will contain few questions on Engineering Mathematics carrying 15 marks.		

**Note:** GATE 2012 score will be valid for **2 YEARS** from the date of announcement of the GATE 2012 results.

No. 569, I-Floor, Near Cambridge Public School  
BALAJI COLCONY, TIRUPATI

Website: leadacademics.com, Phone: 9392926692

**Our Ranks in GATE: 3, 11, 13, 14, 63, 72, 112, 120, 122, 141, 144, 172, 182, 186, 229, .....**  
**JTO – 2008: 7 out of 36 students got Jobs in BSNL through JTO - 2008**

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## Weightage of Marks in GATE (Subject-wise)

### GATE – EC (Electronics & Communication Engineering)

Subject	1 Mark Questions	2 Mark Questions	Total Marks
Engineering Mathematics	3 – 6	3 – 8	9 - 22
Networks	0 – 5	4 – 7	10 - 18
Electronic Devices	2 – 5	3 – 8	8 – 20
Analog Circuits	0 – 5	5 – 11	12 – 25
Digital Circuits	0 – 5	5 – 8	13 – 19
Microprocessors	0 – 1	1 – 4	1 – 8
Signals & Systems	2 – 5	3 – 8	9 – 18
Control Systems	1 – 3	4 – 9	10 – 21
Communications	0 – 5	6 – 14	14 – 31
Electromagnetics	1 – 2	3 – 8	8 – 18
General Aptitude	5	5	15

Note: Analysis is made on the basis of previous **NINE** GATE Papers (2003-2011)

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CS, EC, EE, IN, CE, ME	Q.1 to Q.25: Will carry one mark each (sub-total 25 marks).	$\frac{1}{3}$ mark will be deducted for each wrong answer.
	Q.26 to Q.55: Will carry two marks each (sub-total 60 marks)	$\frac{2}{3}$ mark will be deducted for each wrong answer.
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## Weightage of Marks in GATE (Subject-wise)

### GATE – CS (Computer Science & Information Technology)

Subject	1 Mark Questions	2 Mark Questions	Total Marks
Engineering Mathematics	3 – 6	6 – 15	16 – 35
Digital Logic	1 – 4	0 – 5	2 – 14
Computer Organization & Architecture	0 – 4	3 – 12	8 – 24
Programming & Data Structures	0 – 5	3 – 10	7 – 25
Algorithms	2 – 8	6 – 15	15 – 32
Theory of Computation	0 – 4	3 – 7	9 – 16
Compiler Design	1 – 4	0 – 8	1 – 20
Operating Systems	0 – 3	2 – 8	4 – 17
Databases	0 – 3	3 – 6	5 – 12
Computer Networks	0 – 5	2 – 6	5 – 14
Information Systems & Software Engineering	0 – 1	1 – 2	2 – 5
Web Technologies	0 – 1	-	0 – 1
General Aptitude	5	5	15

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## Weightage of Marks in GATE (Subject-wise)

### GATE – IN (Instrumentation Engineering)

Subject	1 Mark Questions	2 Mark Questions	Total Marks
Engineering Mathematics	2 – 4	5 – 6	12 – 16
Electric Circuits	1 – 2	2 – 8	5 – 18
Measurement Systems	0 – 1	1 - 2	3 – 5
Transducers, Mechanical Measurement & Industrial Instrumentation	1 - 3	3 – 4	12 – 18
Analog Electronics	2 - 4	5 – 7	12 – 18
Digital Electronics	1 - 3	2 – 6	5 – 15
Microprocessors	1	1	3
Signals, Systems & Communication	2 – 3	3 – 8	8 – 19
Electrical & Electronic Measurements	2 - 3	3 – 10	8 – 23
Control Systems & Process Control	1 - 2	5 – 6	11 – 14
Analytical Instrumentation	1 - 2	2 – 4	5 – 10
Optical Instrumentation	1	1 - 2	2 – 5
Biomedical Instrumentation	1	1	3
General Aptitude	5	5	15

**Note:** Analysis is made on the basis of previous GATE Papers

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